Docket No.: 42P10802



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

ARCH D. ROBISON

Application No.: 09/844,345

Filed: April 27, 2001

For: PRUNING LOCAL GRAPHS IN AN

INTER-PROCEDURAL ANALYSIS

**SOLVER** 

Art Group: 2122

Examiner:

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APR 1 2 2002

**Technology Center 2100** 

## INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure, enclosed is a copy of Information Disclosure Statement by Applicant (form PTO/SB/08), which is being submitted before the mailing of a first Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s). Copies of the references cited on PTO/SB/08 are enclosed herewith.

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Thinh V. Nguyen, Reg. No. 42,034

Date: April 04, 2002

12400 Wilshire Blvd., 7th Floor Los Angeles, California 90025 (714) 557-3800 **CERTIFICATE OF MAILING** 

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 to the Patent and Trademark Office on: *April 04, 2002*.

Barbara Hayashi

04/04/02

Date

Substitute for Form 1449A/PTO (Modified)			Complete if Known			
INFORMATION DISCLOSURE			Application Number	09/844,345		
			Filing Date	<del></del>		
STAT	reme	ENT BY APPLYCANT	First Named Inventor:	A/27/01   RECEIVED		
	(use as	many sheets as necessary)	Group Art Unit	2122 APR 1 2 2002		
		APR 0 8 2002 W	Examiner Name	Technic		
Sheet	1	of A	Attorney Docket Number	Technology Center 21		
		OTHER ARDEMO PATE	NT LITERATURE DOCUMEN	TS		
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published				
		COUSOT, P., and COUSOT, R., Abstract Interpretation: A Unified Model for Static Analysis of Programs by Construction or Approximation of Fixpoints, In Conference Record of the Fourth Annual ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages, (Los Angeles, CA, 1977), ACM Press, 238-252  Tool Interface Standards, ELF: Executable and Linkable Format, ftp://ftp.intel.com/tis/elf11g.zip				
		Unix International, Programming Languages SIG. Dwarf Debugging Information Format, Revision 2.0.0 (July 27, 1993), <a href="mailto:ftp://ftp.intel.com/pub/tis/dwf11.zip">ftp://ftp.intel.com/pub/tis/dwf11.zip</a>				
		CHENG, B.C., and HWU, W.M., Modular Interprocedural Pointer Analysis Using Access Paths: Design Implementation and Evaluation. In <i>Proceedings of the SIGPLAN '00 Conference on Programming Language Design and Implementation</i> (Vancouver, British Columbia, June 2000), ACM Press, 57-69.  Edison Design Group, C+++ Front End, Internal Documentation (excerpt), (August 2000), <a href="http://www.edg.com/docs,edg_cpp.pdf">http://www.edg.com/docs,edg_cpp.pdf</a>				
		COOPER, K.D., and KENNEDY, K., and TORCZON, L., Interprocedural Optimization: Eliminating Unnecessary Recompilation. In <i>Proceedings of SIGPLAN '86 Symposium on Compiler Construction</i> (Palo Alto, CA, June 1986) ACM Press 58-67.  BURKE, M., and TORCZON, L, Interprocedural Optimization: Eliminating Unnecessary Recompilation. In <i>ACM Transactions onProgramming Languages and Systems</i> , Vol. 15(3), 367-399				
		HALL, M.W., HIRANANDANI, S., K Compilation of Fortran D for MIMD 522-534				
		POLLOCK, L.L., and SOFFIA, M.L. Transactions on Programming Lan				
		GOUGH, K.J., and KLAEREN, H., proceeding of 1997 Joint Modular L Computer Science, Vol. 1204, 41-5	.anguages Conference (Linz, A			
* 180						

Examiner	Date	•
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